

FIG. 1 is a schematic diagram of a device 100. The device 100 includes a substrate 10 and a top layer 14. A central region 15 is defined by a dashed line. Within this region, there are two groups of elements 19. Each group 19 includes a top element 18 and a bottom element 18, connected by a vertical line 28. A central element 20 is located between the two groups 19. The central element 20 is connected to the top and bottom elements 18 by lines 16. A voltage source V2 is connected to the central element 20. The device 100 also includes a bottom layer 10 and a top layer 14. The central region 15 is defined by a dashed line. Within this region, there are two groups of elements 19. Each group 19 includes a top element 18 and a bottom element 18, connected by a vertical line 28. A central element 20 is located between the two groups 19. The central element 20 is connected to the top and bottom elements 18 by lines 16. A voltage source V2 is connected to the central element 20.

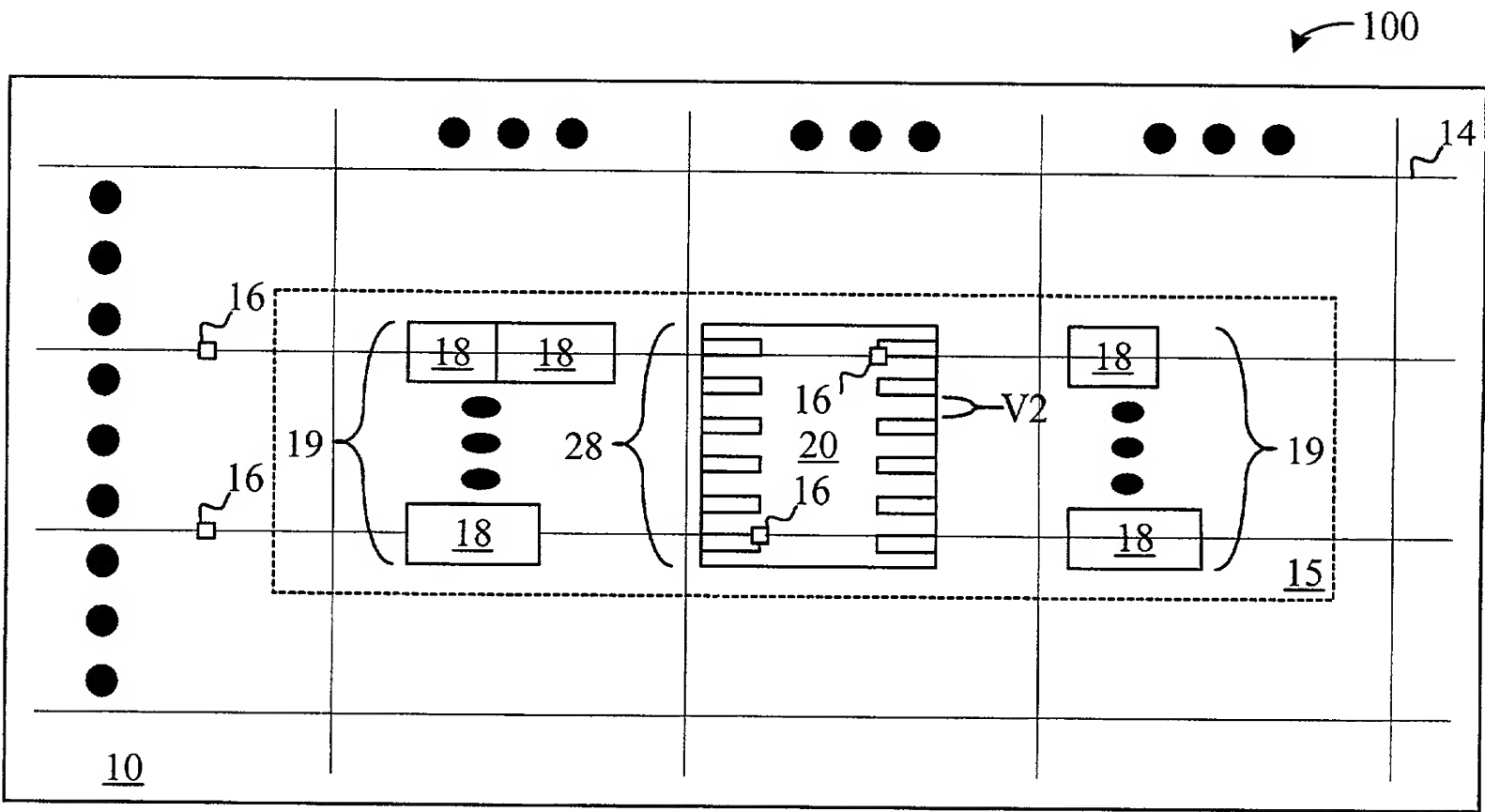


FIG. 1

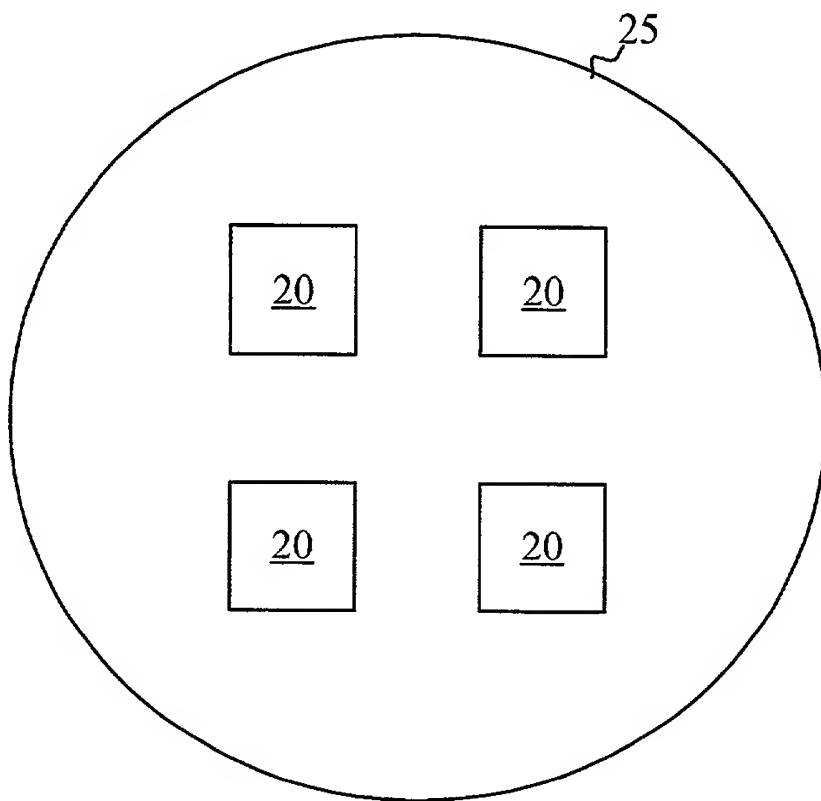


FIG. 2
(PRIOR ART)

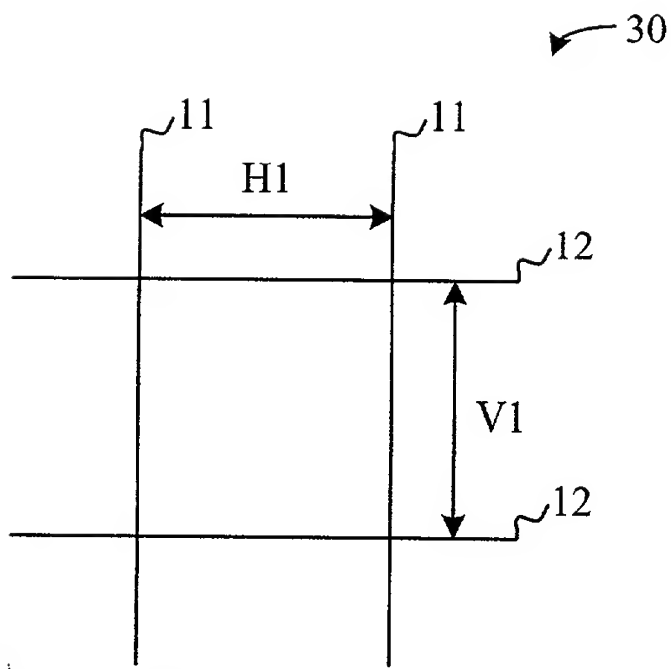


FIG. 3A
(PRIOR ART)

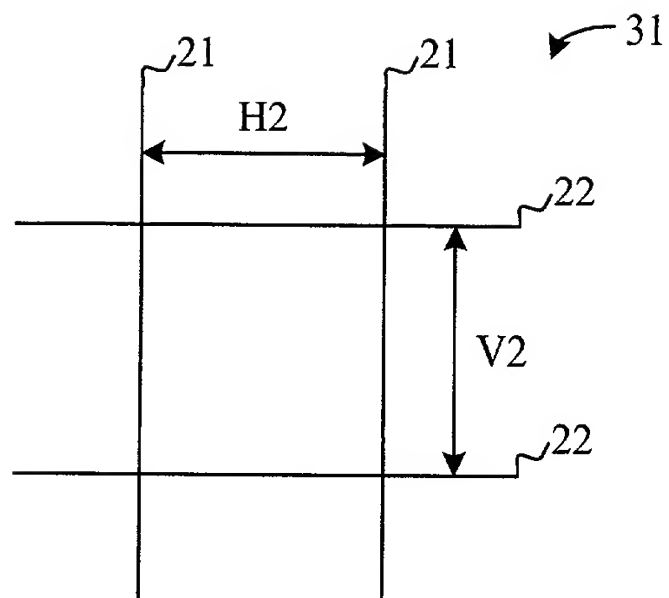


FIG. 3B
(PRIOR ART)

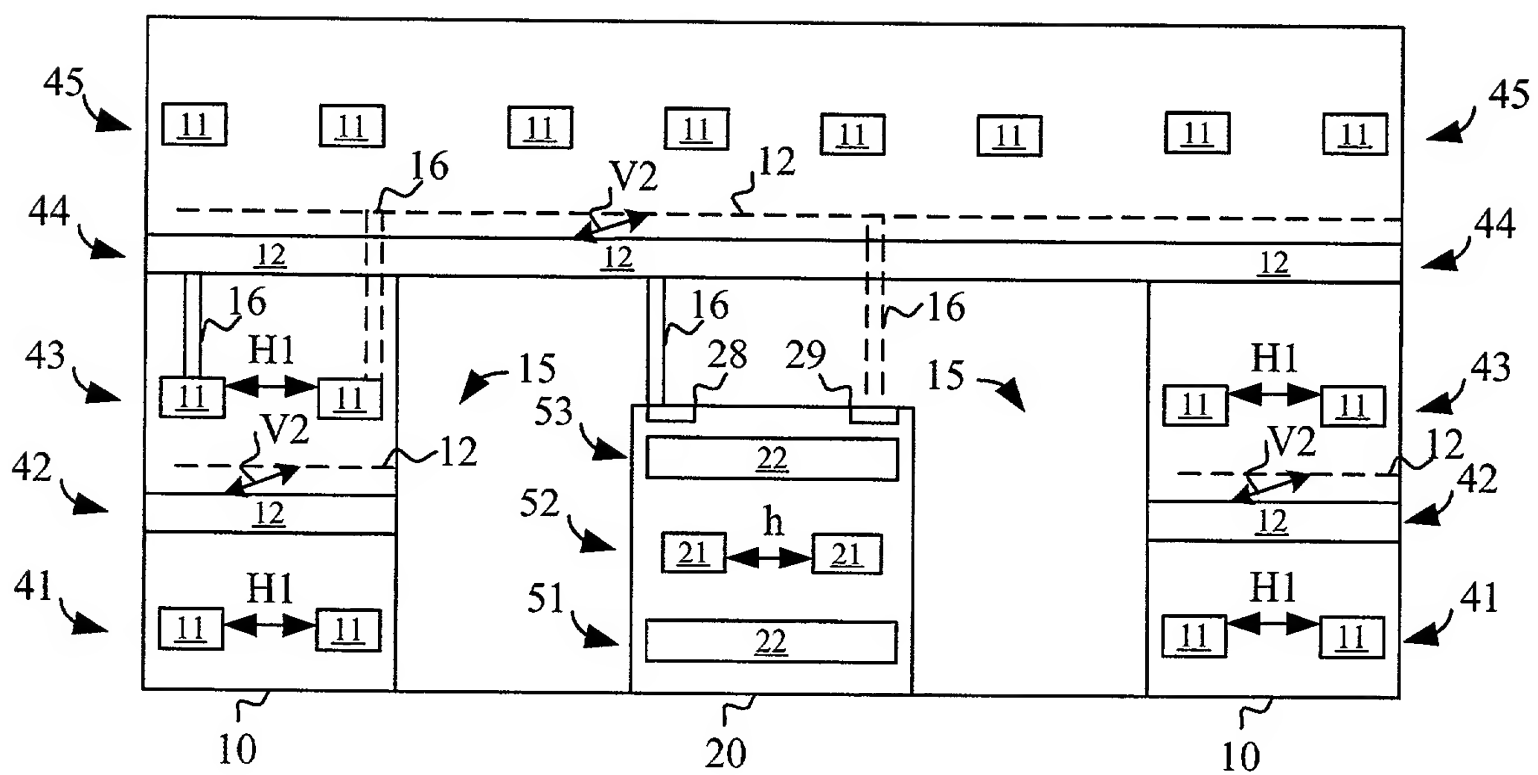


FIG. 4

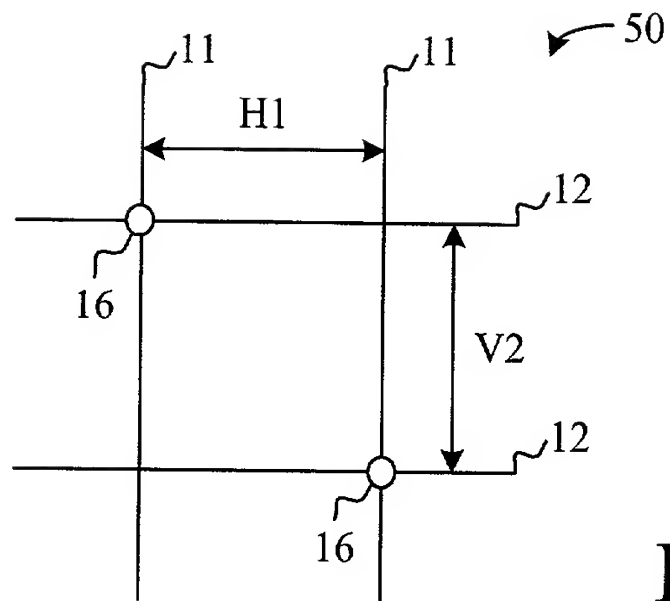


FIG. 5

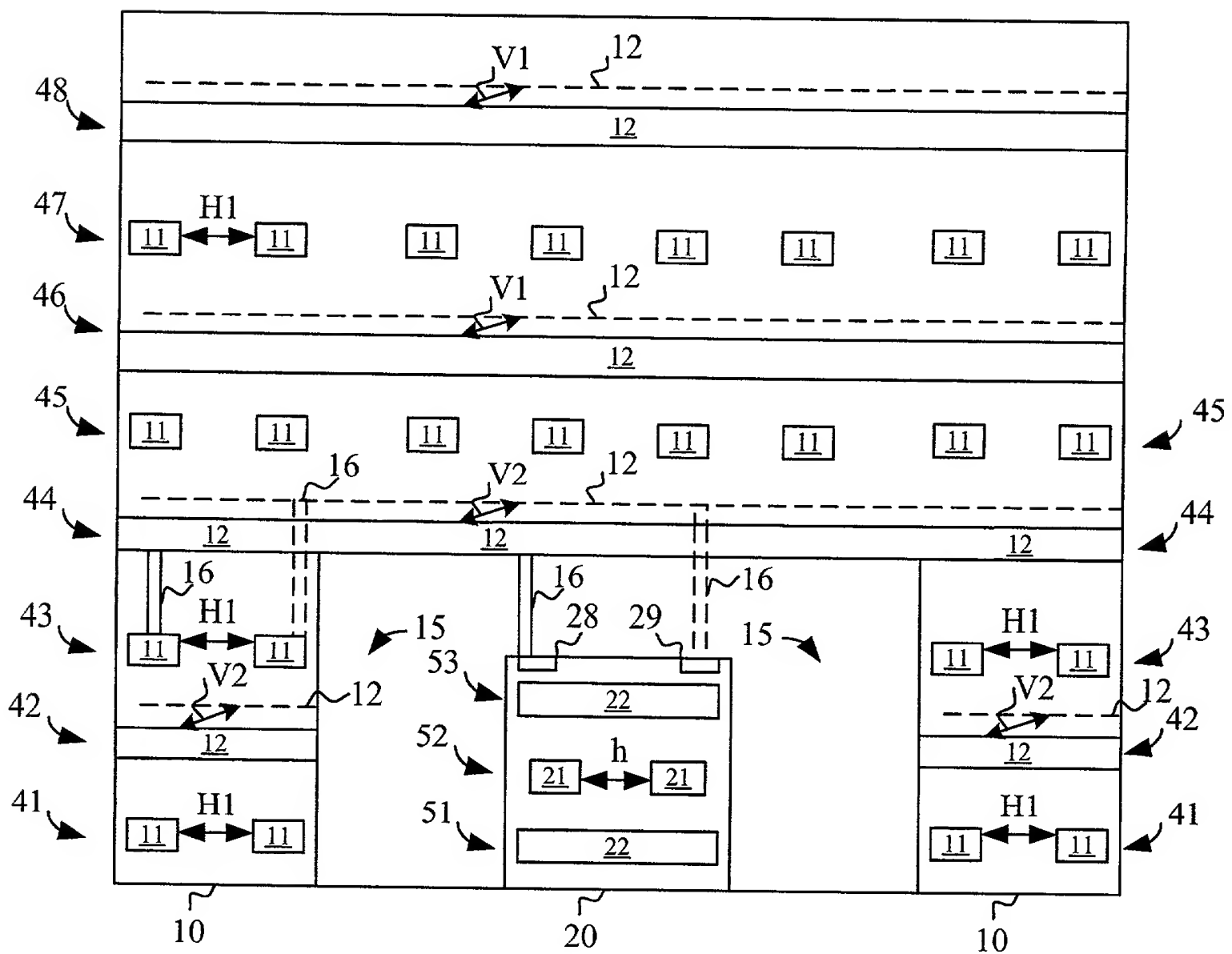


FIG. 6

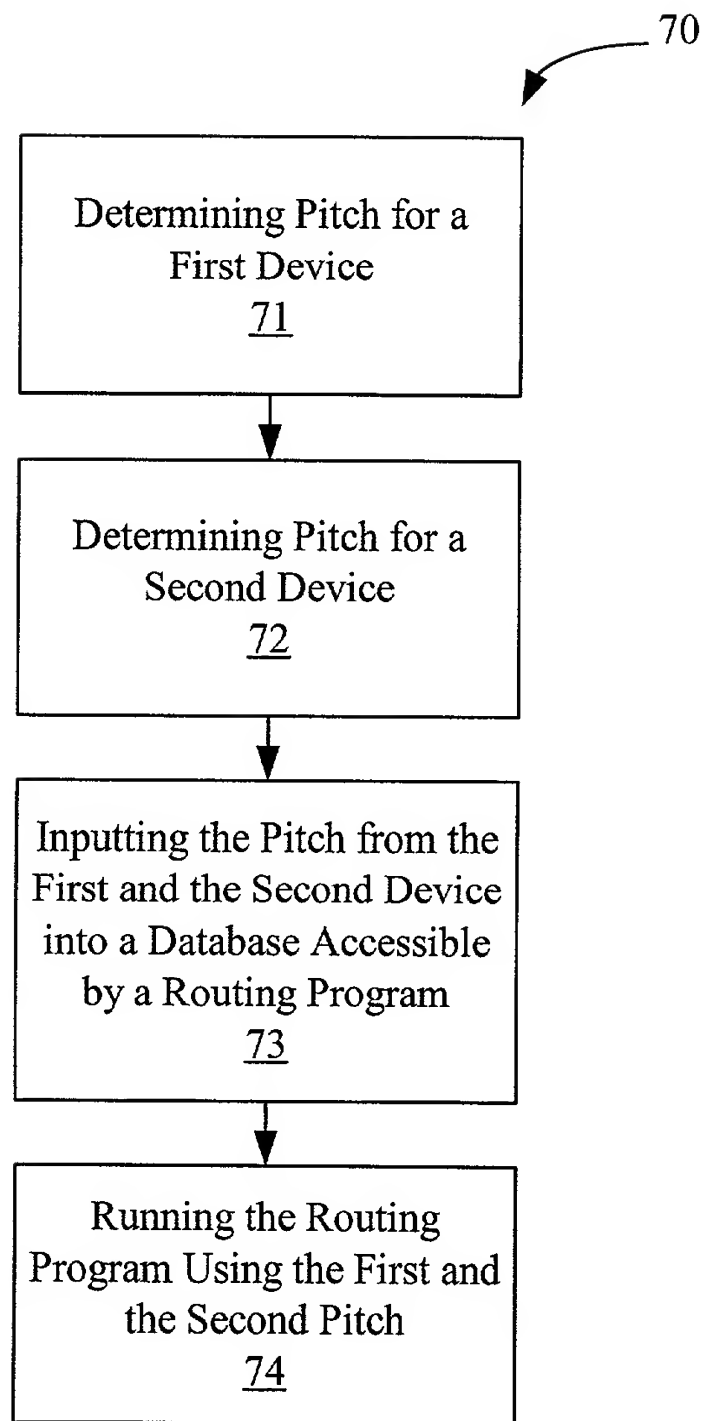


FIG. 7